

Supercurrent and Phase Slips in a Ballistic Carbon Nanotube Bundle Embedded into a van der Waals Heterostructure

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ABSTRACT: We demonstrate long-range superconducting correlations in a several-micrometers-long carbon nanotube bundle encapsulated in a van der Waals stack between hBN and NbSe₂. We show that a substantial supercurrent flows through the nanotube section beneath the NbSe₂ crystal as well as through the 2 μ m long section not in contact with it. The large in-plane critical magnetic field of this supercurrent is an indication that even inside the carbon nanotube Cooper pairs enjoy a degree of paramagnetic protection typical of the parent Ising superconductor. As expected for superconductors of nanoscopic cross section, the current-induced breakdown of superconductivity is characterized by resistance steps due to the nucleation of phase slip centers. All elements of our hybrid device are active building blocks of several recently proposed setups for realization of Majorana fermions in carbon nanotubes.

KEYWORDS: 1D superconductivity, phase slip centers, phase slip lines, carbon nanotubes, niobium diselenide, van der Waals superconductors

A mong the many schemes proposed^{1,2} for the demonstration of Majorana fermions (MFs), the most popular consists of a 1D superconductor with large spin-orbit interaction (SOI) in the presence of a magnetic field. It was in such systems that the first signatures of Majorana quasiparticles were reported.³⁻⁹ The 1D conductor is typically a III-V semiconducting nanowire proximitized by Al. In the past years, several works¹⁰⁻¹⁵ have pointed out the advantage of using carbon nanotubes (CNTs) instead. They are, in fact, genuinely one-dimensional,¹⁶ since their cross section comprises only a handful of atoms: as a consequence, they have well-separated transverse modes. Also, clean CNTs provide ballistic transport¹⁷ and, owing to their curvature, show a sizable spin-orbit field parallel to their axis.¹⁸⁻²³ From the theoretical point of view, their spectral properties can be suitably computed at a microscopic level.

So far, clean transport features in CNTs were mainly demonstrated on suspended carbon nanotubes.^{24–30} However, recent theoretical works^{13,15} have proposed the combination of CNTs with superconducting transition metal dichalcogenides (such as NbSe₂). This is clearly incompatible with the

disorder-free scheme based on suspended CNTs. This situation is somehow similar to that of graphene one decade ago: high mobility devices required graphene suspended above the substrate, ^{31,32} while the observation of new physics often required the combination of graphene with other materials. The impasse was resolved by the introduction of hBN encapsulation,³³ a crucial qualitative leap in the fabrication of the so-called van der Waals heterostructures.^{34,35}

In this work, we apply ideas and methods of van der Waals stacking to CNTs, demonstrating a hybrid 1D-2D superconducting heterostructure. We investigate a CNT encapsulated between a few-layer NbSe₂ crystal and a hBN flake on a graphite substrate. We demonstrate that superconducting

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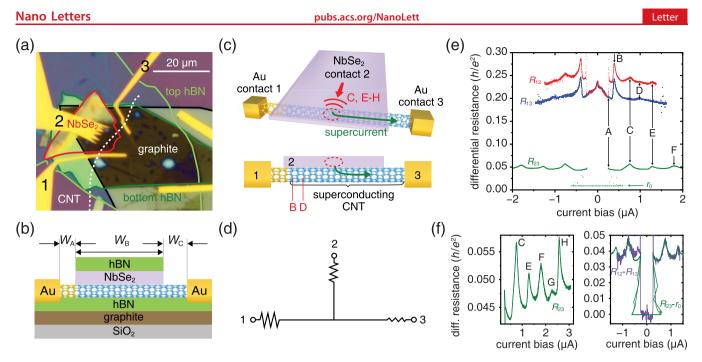


Figure 1. (a) Optical micrograph of the final device. The boundaries of the different crystals are indicated by the color lines. Electrodes 1 and 3 contact the carbon nanotube (CNT), while 2 contacts the few-layer NbSe₂ crystal. (b) Stacking scheme of the van der Waals heterostructure. The lengths of the three CNT portions are $W_A = 0.2 \mu m$, $W_B = 7.5 \mu m$, and $W_C = 2 \mu m$. (c) Schematics representing the location of phase slip lines and centers. A region of higher transparency (ellipse) between CNT and NbSe₂ implements an analogue of the point contact experiment in ref 36. The phase slip centers (see text) producing the resistance peaks B and D are located somewhere in the CNT portion between NbSe₂ and contact 1. The region between NbSe₂ and contact 3 appears to be perfectly superconducting, with no signature of resistance. (d) Electrical replacement scheme of the device. (e) Two-terminal differential resistance versus current bias between contacts $1-2(R_{12}, \text{red})$, $1-3(R_{13}, \text{blue})$, and $2-3(R_{23}, \text{green})$. (f) The left panel zooms in on the high bias part of the R_{23} curve in (d). In the right panel, we plot the difference between R_{12} and R_{13} , together with $R_{23} - r_0$ where r_0 is the residual low-bias resistance in R_{23} . The perfect match supports the electrical scheme shown in (d).

correlations extend throughout the whole CNT length, including the few micrometer-long portions next to the $NbSe_2$ crystal. At finite current bias, a series of resistance steps is observed, indicating the nucleation of phase slip centers, as expected for a quantum wire of nanometric cross section.

Our device is fabricated starting from a flake of graphite and one of hBN exfoliated and stamped on a standard Si/SiO₂ chip using the dry stamping technique described in ref 37. As in refs 38 and 39, the graphite/hBN stack provides an atomically flat conductive back gate capable of screening charge traps at the SiO₂ surface as well as disorder from contaminants near the CNT. This is due to the close proximity between CNT and graphite, which is given by the thickness (\approx 35 nm) of the bottom hBN layer.

On a separate chip, macroscopically long CNTs are grown using a standard recipe for clean single wall CNTs described elsewhere.³⁰ The growth chip with CNTs is then imaged by scanning electron microscopy, and a suitable CNT is picked up by a polydimethylsiloxane (PDMS) stamp coated with a polycarbonate (PC) layer, following the standard technique³³ used in van der Waals heterostructure fabrication. The application of the pick-up method to CNTs has been reported by Huang et al.⁴⁰ Unlike these authors, we observe a weak adhesion between CNT and hBN; therefore we pick up the CNT directly by the PC/PDMS stamp. After dissolving the PC in chloroform, a topography scan by atomic force microscopy (AFM) revealed no significant trace of contamination. Then, pick-up stamping in N2 atmosphere is used to deposit a fewlayer NbSe₂ flake (capped with a thinner hBN crystal) onto the CNT. Gold edge contacts to CNT and NbSe₂ are fabricated simultaneously using the recipe established for encapsulated graphene.³³ Despite the CNT–Au interface being limited to a handful of atoms, very transparent contacts can be obtained which support currents of hundreds of microamperes. Further information about sample fabrication is provided in the Supporting Information.

The completed sample is shown in Figure 1a and illustrated schematically in Figure 1b,c. It has three terminals: two are Au edge contacts and one is provided by the NbSe₂ flake in contact with the CNT along a distance of $W_{\rm B}$ = 7.5 μ m. This central part is located between two uncovered CNT portions whose length is $W_A = 0.2 \ \mu m$ and $W_C = 2 \ \mu m$, respectively (see Figure 1b). The sample is mounted on the coldfinger of a dilution refrigerator with carefully filtered DC transport lines. We measure two-terminal differential conductance at finite bias for different temperatures or magnetic fields. Current is measured between the pairs of contacts 1-2, 2-3, or 1-3; see Figure 1c. When bias is applied to a pair of contacts, the remaining third one is left open. The comparison between the conductance measurement in the three configurations disentangles contributions from different parts of the device. Figure 1e shows the current dependence of the differential resistance $R_{ij} = \partial V_{ij} / \partial I_{ij}$, where V_{ij} and I_{ij} are the voltage and the current between the contacts i and j, respectively. The measurement is performed at base temperature (T = 30 mK)and in the absence of a magnetic field. From Figure 1e, we deduce that, in good approximation, $R_{12} \approx R_{13} + R_{23}$; see also the right panel of Figure 1f. More precisely $R_{12} - R_{13} = R_{23} - R_{13}$ r_0 where r_0 is the low-bias value of R_{23} . From the electrical point of view, this corresponds to a three-terminal circuit sketched in Figure 1d, where the resistance associated with the contact 3 is negligible.

All the traces show steps in resistance which are separated by peaks, as shown in Figure 1e. The positions of these features are indeed controlled by current and not by voltage bias. In fact, they are nicely aligned only when plotted as a function of current; see Supporting Information. Moreover, they occur for voltage bias larger than any relevant energy scale, e.g., the gap of bulk NbSe₂, which is approximately 1 meV. This excludes spectroscopic features such as Andreev reflection or tunneling as a possible source of the observed conductance modulation. The measured resistance traces appear very similar to those observed in the literature $^{41-43}$ for short CNT-based Josephson junctions and are attributed to phase slip events, as discussed below. At high bias, the resistance is of the order of 0.2 h/e^2 between contacts 1-2 and 1-3, while it is much lower between contacts 2–3, i.e. about 0.05 h/e^2 . This corresponds to a conductance of $\approx 20 \ e^2/h \approx (1.3 \ k\Omega)^{-1}$, i.e., 5 times the theoretical limit for a single undoped CNT (that is, $4e^2/h$ per spin- and valley-degenerate channel). A reason for such a large conductance could be that we picked up indeed a bundle of few CNTs. This large conductance is nevertheless small compared to the impressive value at low bias: for current below 600 nA the two terminal resistance is $R_{23} \equiv r_0 = 0.0087 \ h/e^2$, or 225 Ω , which corresponds to a conductance of nearly 120 e^2/h . Indeed the device conductance is much larger than that, since out of 225 Ω at least 80 Ω are due to the cryostat cables while the Au-NbSe₂ contact resistance is unknown but estimated to be of the order of 100 Ω based on our previous experience with similar contacts. Therefore, we estimate the two-terminal conductance of the device to be at most of the order of tens of ohms, corresponding to a conductance several hundreds of e^2/h . In the normal state, this conductance would require a many-nm-thick bundle of CNTs. By AFM scans (shown in the Supporting Information), we measured a CNT diameter of approximately 1 nm, excluding a thick bundle of many CNTs. The presence of only a few CNTs is in agreement with the high bias, normal state conductance of the CNT which is of the order of 18 e^2/h ; see Figure 1e. Finally, as discussed in the Supporting Information, this large conductance is currentindependent up to the critical value. This suggests attributing the small residual r_0 entirely to cryostat cables and Au contact resistance. The observations above indicate that the large lowbias conductance is produced by a finite supercurrent between contacts 2 and 3. The supercurrent and the phase slip resistance steps unambiguously show that the CNT is superconducting. Since any normal-conducting CNT section between the contacts 2 and 3 would limit the two-terminal conductance to $4e^2/h$ per spin- and valley-degenerate channel, we conclude that the CNT must be superconducting up to the very interface with the Au edge contact 3. This is possible if the contact transparency at the edge contact 3 is relatively high but not too close to 1: in this case both inverse proximity (reducing the gap in the CNT) and direct proximity (proximitizing the Au region close to the interface 3) would be at work, producing a supercurrent all the way up to the Au contacts.

The giant conductance measured between contacts 2 and 3 is not the only signature of proximity-induced superconductivity in the CNT. An independent proof of this is given by the emergence of current-driven steps in conductance due to nucleation of phase slip centers.^{44–48} Such steps were reported since the very first observation of supercurrent in CNT-based Josephson junctions.^{41,42} On the other hand, at sufficiently high current densities, transport characteristics in few-layer NbSe₂ show similar step-like traces due to nucleation of phase slip *lines* (PSL).^{36,49–52} To discriminate between PSCs and PSLs, we need to carefully compare the three traces in Figure 1e. As shown below, we can approximately locate the position of the sources of the dissipative features. From the data we can conclude that (i) the features B and D indicate phase slip centers within the CNT, located in the 1-2 portion, as they are visible in the 1-2 and 1-3 traces but not in 2-3. (ii) The features C, E, F, G, and H are located on the NbSe₂ side, (visible in the 2-3 and 1-2 traces but absent in 1-3). We interpret these steps as PSLs in NbSe₂. The relatively low current necessary to trigger the PSL nucleation (compared to ref 36) suggests that the portion of the CNT where the contact to NbSe₂ is particularly transparent is relatively narrow (see sketch in Figure 1c). (iii) The interpretation of feature A, namely, the supercurrent jump, is more difficult but of crucial importance. For current bias above 600 nA the resistance R_{23} acquires a finite value of nearly 0.05 h/e^2 , which is, as discussed above, approximately the difference between R_{12} and R_{23} . This value is basically the contact resistance between the multimode NbSe₂ and the few-mode CNT, which is the only source of resistance in a perfectly ballistic quantum wire. Notice that all measured resistive features in R_{23} are also present in the difference between R_{12} and R_{13} (see Figure 1f, right panel), indicating that no relevant scattering processes are taking place in the portion 2-3.

The supercurrent between contacts 2 and 3 is flowing through the 2 μ m long CNT segment W_C not in contact with NbSe₂. To explain a supercurrent in this segment there are two possibilities: either the superconductivity in this region is proximity-induced or intrinsic. The former case is the most intuitive: the transparent NbSe2-CNT interface induces by proximity effect superconducting correlations in the nearby CNT segment. This lateral proximity effect can extend up to a length $L_{\rm N} = \min(L_{\rm T}, L_{\phi})$, where $L_{\rm T} = \hbar v_{\rm F}/(k_{\rm B}T)$ is the thermal length in the ballistic case, and L_{ϕ} is the coherence length in the CNT. Assuming for the Fermi velocity $v_{\rm F} = 10^6$ m/s, we find that for T < 1 K, $L_T > 7.7 \mu m$, compatible with our device size. The absence of normal conducting regions in the CNT portion between 2 and 3 implies that the Au contact only reduces, by inverse proximity, the order parameter on the CNT side of the Au–CNT interface, without fully suppressing

The alternative scenario is that of an intrinsic superconductivity of the CNT, which was reported^{42,43,53–56} for thick CNT *bundles* or arrays of many CNTs. Indeed, the bundle nature of the CNTs is suggested by the large high-bias (normal) conductance between contacts 2 and 3, which indicates the presence of several channels, as discussed above. Sufficiently long CNT bundles have been reported to be intrinsically superconducting,^{43,56} with a large variety of critical temperatures up to several kelvins. However, all the reported bundles contained a large number of CNTs, of the order of many tens up to hundreds, clearly incompatible with our case. Besides, the intrinsic superconductivity scenario can be excluded by comparing conductance measurements in-plane and out-of-plane magnetic field, as discussed below.

On the basis of the comparison of the resistance traces between contacts 1-2, 1-3, and 2-3 in Figure 1e, we argued above that the features (B, D) and (C, E, F, G, H) originate from PSCs and PSLs, respectively. On the other hand,

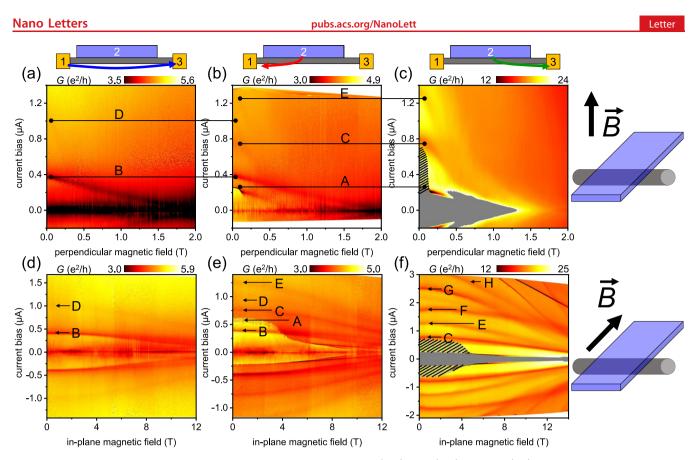


Figure 2. The graphs show the differential conductance between contacts 1-3 (a, d), 1-2 (b, e), and 2-3 (c, f) as a function of current bias and perpendicular (a, b, c) or in-plane (d, e, f) magnetic field. The in-plane magnetic field is approximately perpendicular to the CNT axis. In the first row the graphs have the same scale for the current axis. We observe that the features have the same current and field dependence in the different contact configurations. This allows us to identify the same feature in different graphs and assign to it the same label (horizontal lines). In (c) and (f) conductance saturating beyond $25 e^2/h$ is depicted in gray. Dashed areas in (c) and (f) are regions corresponding to the situation where one current bias value corresponds to two conductance values. This is due to the fact that we have a voltage resistance connected to a voltage source; see text.

resistance steps produced by PSCs in CNTs and PSLs in NbSe₂ are expected to differ in their perpendicular magnetic field dependence. In particular, the current triggering a PSC in a CNT decreases almost linearly with the magnetic field, and the associated resistance peak is visible up to fields comparable to the critical field in NbSe₂.^{41,42} In contrast, the current triggering a PSL weakly depends on the magnetic field for low fields, displaying a downward curvature, and already at moderate fields the PSL feature is washed out altogether.³⁶

To corroborate the above attribution, we have performed finite-bias conductance measurements as a function of in-plane and out-of-plane magnetic field. Panels a, b, and c of Figure 2 refer respectively to contact configurations 1-3, 1-2, and 2-3, measured with magnetic field applied *out-of-plane*, while panels d, e, and f refer to the same respective contact configuration, measured with magnetic field applied *in-plane*. The conductance measurements with in-plane magnetic field (Figure 2d,e,f) were performed in a second cool-down, necessary to rotate the sample.

As shown in Figure 2a feature B is visible up to 2 T, where it merges with the zero-bias conductance dip (which is discussed below). It decreases almost linearly with the magnetic field, similar to the conductance steps in short CNT Josephson junctions.^{41–43} The feature D is instead very faint and cannot be properly discerned.

Interestingly, the features C, E, F, G, and H, which we attributed to PSLs, disappear at $B_{\perp} \approx 0.3$ T (see Figure 2b,c).

This is consistent with the behavior observed³⁶ in plain fewlayer NbSe₂ and predicted by time-dependent Ginzburg– Landau simulations by Berdiyorov et al.,^{57,58} who attributed the smearing to the introduction of Abrikosov vortices.

On the other hand, application of an in-plane field *does not* wash out such PSL-related features, since no significant amount of vortices is introduced by an in-plane field as the NbSe₂ thickness is smaller than the coherence length. Instead, the in-plane field simply reduces the gap and correspondingly the current thresholds for the nucleation of PSLs. Since NbSe₂ is an Ising superconductor (singlet Cooper pairs protected by spin-valley locking), one needs very large parallel fields to quench the superconducting gap, much larger than the maximum field available in our setup. ⁵⁹⁻⁶¹ As a consequence, in Figure 2f the PSL conductance dips are observed for lower bias at high field, but the reduction is modest even for the largest magnetic field experimentally accessible, i.e., 14 T.

The behavior of the feature B in out-of-plane and in-plane magnetic field (shown in Figure 2a,d) rules out the possibility of intrinsic superconductivity in the CNT. In fact, from the CNT point of view in both cases the magnetic field vector is perpendicular to the axis; therefore one would expect no difference in the magnetic field dependence. It is evident that the striking critical field enhancement for this feature in Figure 2d reflects the Ising behavior of the parent superconductor.

We stress that in our measurements we apply a voltage bias: when applied between contacts 1 and 2 or between 1 and 3, the resistance of contact 1 is sufficiently large that current and voltage bias are roughly proportional. However, for contacts 2-3 the device resistance is so low that at the critical value for the supercurrent the resistance increase produces a marked decrease of current. The heating power produces a jump to the overheated part of the voltage-current characteristics. Thus, the $R_{23}(I)$ curve is not single-valued, as shown in Figure 1e,f. In the color plots of Figure 2c,f such two-valued regions (dashed in the graphs) cannot be properly represented.

A comment is in order about the relatively broad zero bias resistance peak (conductance dip) between terminals 1-2 and 1-3. This peak seems to be related to a potential barrier of finite height and width at the contact 1. Such zero-bias anomaly has been observed also in short CNT Josephson junctions.⁴² It is not related to superconductivity: it persists, in fact, up to magnetic fields well beyond the critical value, as shown above.

Finally, we turn to the temperature dependence of the features discussed above. Figure 3a shows, for selected

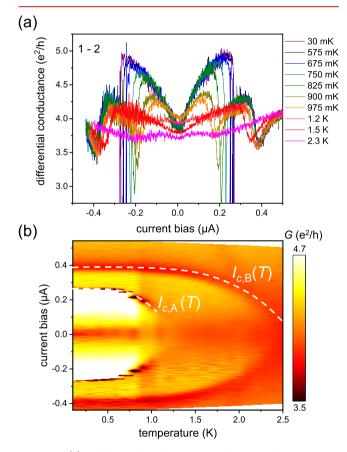


Figure 3. (a) Differential conductance as a function of current bias between contacts 1 and 2, plotted for selected temperatures. (b) Color plot of the differential conductance between contacts 1 and 2, plotted as a function of current and temperature. The plot clearly shows the temperature dependence of the critical current bias I_A and I_B triggering features A and B, respectively.

temperatures, the conductance versus bias for the contact configuration 1–2. The measured current range allows us to follow the temperature dependence of features A and B. Clearly, with increasing temperature, their critical currents $I_{c,A}$ and $I_{c,B}$ decrease and their visibility fades out. However, the temperature dependence is different for the two features. This is better visible in the corresponding color plot of Figure 3b,

where the zero-bias dip and the features A and B are clearly visible. Interestingly, the critical current $I_{c,B}(T)$ has a temperature dependence very similar to that reported for suspended proximitized CNTs,^{41,42} i.e., a weak dependence at low temperature and a pronounced decrease close to a critical temperature, which is $\simeq 2.6$ K in our case. We emphasize the remarkable similarity of both magnetic field (linear dependence as shown by Figure 2a) and temperature dependence of $I_{c,B}$ with the corresponding feature observed in short suspended CNTs; e.g., see Figure 4B in ref 41 and Figure 9 in ref 42.

The critical value for the supercurrent (feature A) shows a similar temperature dependence but with a T_c that can be extrapolated toward approximately 1.5 K, i.e., about half of that for feature B; see Figure 3b. Our picture for feature A is that it represents the junction between NbSe₂ and CNT. This point shows the least critical current $I_{c,A}$, representing the weakest point in our 1D superconductor. With this picture in mind, it is interesting to check whether the measured critical current $I_{c,A}$ corresponds to that expected for a junction of transparency \overline{T} in a 1D conductors with N channels with 4-fold degeneracy,⁶²

$$I_{\rm c} \simeq \overline{T} N \frac{e \Delta^*}{\hbar} \tag{1}$$

where for Δ^* we shall take $\Delta^* = 1.764k_B \times 2.6 \text{ K} = 395 \ \mu\text{eV}$, the BCS gap corresponding to $T_c = 2.6 \text{ K}$ as for $I_{c,B}$. With $I_c = 600 \text{ nA}$, we deduce a $\overline{T}N$ product of 6.2. This value is very close to the one deduced from the normal conductance between contacts 2 and 3. For this we take the reciprocal of the resistance minimum between the resistive jump A and the peak *C*, i.e.,

$$G = (0.043h/e^2)^{-1} = 5.8 \times 4e^2/h$$

which implies a $\overline{T}N$ product of 5.8.

In conclusion, we have demonstrated a long-range lateral proximity effect in a hybrid 1D–2D van der Waals heterostructure consisting of NbSe₂ and a macroscopically long carbon nanotube. The low level of disorder and the high contact transparency enable the observation of a large supercurrent through the carbon nanotube, including the 2 μ m long section not directly in contact with NbSe₂. Finite current bias triggers distinctive resistance peaks, which we identify as phase slip centers, clear signatures of superconductivity within the carbon nanotube.

ASSOCIATED CONTENT

③ Supporting Information

The Supporting Information is available free of charge at https://pubs.acs.org/doi/10.1021/acs.nanolett.1c02565.

Carbon nanotube growth, details about the stamping process, edge contact fabrication, atomic force microscopy images, voltage vs current bias dependence of the resistive peaks, and temperature dependence of $G_{1-3}(I)$ and $G_{2-3}(I)$ (PDF)

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Notes

The authors declare no competing financial interest.

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Supporting Information: Supercurrent and Phase Slips in a Ballistic Carbon Nanotube Bundle Embedded into a van der Waals Heterostructure

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CARBON NANOTUBE GROWTH FROM THIN CO FILMS USING CHEMICAL VAPOR DEPOSITION

An array of reference markers (20 nm $Re_{70}Mo_{30}$, 5 nm Pt, 50 μ m spacing) is patterned on a dedicated p-doped Si chip with a 285 nm thick capping layer of thermal SiO₂ using standard electron beam lithography (EBL), sputtering and electron beam evaporation techniques. These markers serve as reference for subsequent fabrication of catalyst dots, localization of grown carbon nanotubes (CNTs) by scanning electron microscopy (SEM), as well as for the CNT pick-up process.

Next, catalyst dot structures (diameter 4 μ m, nominal thickness 1 nm) are prepared from thin Co films by EBL and thermal evaporation. CNTs are grown [1] by chemical vapor depositions (CVD) at 950 °C for 20 minutes. Methane is used as carbon feed stock (flow 10 sccm) and hydrogen as carrier gas (flow 20 sccm). The interior of the reaction chamber is kept oxygen-free during heating and cooling by an argon flow of 1500 sccm.

CNTs are located on the substrate with respect to the marker array by SEM imaging (aperture 30 μ m, acceleration voltage 2 kV, working distance 6 mm, in-lens detector). Under these conditions, typically a 10 up to several 100 μ m long CNT grows from about every hundredth catalyst point, often relatively straight and in the gas flow direction.

DETAILS ABOUT THE STAMPING PROCESS

Exfoliation on PDMS

The fabrication of a van der Waals stack starts with a few mm large bulk crystal. Commercially available blue Nittotape (SPV 224P, as of now simply 'blue tape') is used for efficient thinning of the bulk crystal down to the few-layer regime. By bringing the blue tape and the bulk crystal into contact and retracting again, we can pull off several macroscopic portions of the bulk crystal onto the blue tape. This produces the so-called 0th crystal generation. In order to further thin down the crystals we repeatedly (3-5 times) cleave the crystals using fresh blue tape every time. This results in the subsequent 1st to 5th generations. The blue tape with most promising generation is put onto and then retracted quickly from a rectangular ($\sim 8 \text{ mm} \times 8 \text{ mm}$) thin film of polydimethylsiloxane (PDMS). The PDMS was prepared on a glass slide before. Typically, we observe the best results with the 2nd or 3rd generations. Lastly, we search under the optical microscope for flakes suiting our purposes with respect to size, thickness and other surrounding flakes. The NbSe₂ crystals were bought from $hq^+graphene$.

Direct 2D material stamping onto Si/SiO₂ substrate

We follow the approach of Ref. [2]. As target substrate for our stamping process we use 4.5 mm ×4.5 mm chips cut from p-Si/SiO₂ wafer. The oxide layer is 285 nm thick. On these chips a rectangular array of alignment markers with a x, yspacing of 50 μ m is patterned by means of standard EBL and thermal metal evaporation (1 nm Cr, 30 nm Au). For stamping, we fix the wafer chips on common double-sided tape on top of a x, y, z-piezo positioner. To visually monitor the process, we use an adjustable zoom lens paired with a camera livestreaming to a TV screen [2]. The glass slide with the desired flake on the PDMS is mounted into a micro-manipulator stage and positioned just above the target substrate. The actual stamping is controlled by micrometer screws, while the piezostack is used for rough adjustments. To begin the stamping process, we approach the substrate with the glass slide/PDMS, stopping it when contact is established. The glass slide is then further lowered, in a slow fashion in order to keep control of the movement of the contact area front, which moves across the substrate. This front line is moved across the desired flake. Once the whole flake has made good contact with the substrate the motion of the contact area front is reversed by retracting the glass slide. When the whole flake sticks, the PDMS film is retracted completely. One can use this technique to subsequently stamp multiple flakes on top of each other. In our devices we use this direct stamping method to prepare the graphite/bottom hBN layers for the CNT.

Pick-up technique for 2D crystals and CNTs

We follow the approach of Ref. [3]. The pick-up technique can be used to transfer a sequentially picked-up 2D flakes from a Si/SiO₂ substrate to another. In our approach we apply this method also to CNTs. A lens-shaped PDMS stamp is covered by a thin polycarbonate (PC) film. The PDMS lens is fabricated by placing single drops of PDMS base material and PDMS curing agent onto a glass slide and baking out for 10 minutes on a hot plate set to 150 °C. We fabricate the PC film by sandwiching a handful of drops of 5 % PC/chloroform solution (by weight) between two glass slides. Once all the air between the slides is expelled, we

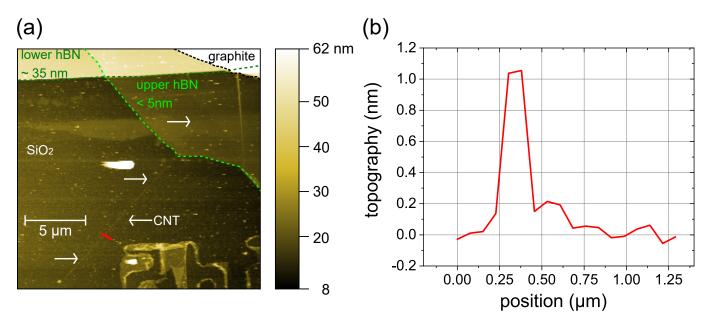


FIG. S1. (a) Atomic force microscopy (AFM) topography scan showing the carbon nanotube (CNT, white arrows) transferred onto the graphite and the lower hBN flake. The AFM scan has been performed just after the stamping process and before the fabrication of the electrodes. The scan is performed at the periphery of the device, where the (CNT) is not covered by the van der Waals crystals and is thus accessible to AFM topography. The boundary of the crystals (whose thickness is reported) is indicated with a dashed line. (b) Line cut of the AFM topography data corresponding to the red segment in (a). We deduce a CNT diameter of approximately 1.2 ± 0.5 nm. Despite the sizable error bar, we can exclude a thick bundle of many CNTs.

smoothly and quickly slide of the top glass slide and a thin and even film of PC/chloroform solution remains on the bottom slide. To drive off the solvent we bake out the slide on a hotplate set to 150 °C for 5 minutes. The resulting PC film can be stored in air until use. We use common scotch tape to transfer the PC film onto the PDMS lens. First, we punch a hole in the scotch tape with an office perforator. Second, using the scotch tape we lift the PC film from the glass slide so, that the film spans over the hole without tearing. We fix the scotch tape with the PC film onto the PDMS lens using more tape.

To provide temperature control during the procedure we used a home-made chip holder made of copper on top of the piezo stack. It includes a DC powered heating resistor and a thermometer. An orifice connected to a vacuum pump provides the necessary suction to fix the substrates to the copper block. We load the glass slide with the PC layer facing down into the micromanipulator stage and position the tip of the PDMS lens above the flake/CNT we want to pick up. Before starting the pick-up process, the temperature of the sample holder on the piezo stack must be below 50 °C as otherwise the PC layer suffers from premature sagging. First, we approach in such a way, that the tip of the PC layer/PDMS lens touches the substrate next to the flake we want to pick up (about 100 μ m away). The desired flake/CNT shall not be touched with the PC layer at this stage. Then the copper block is heated up up to 124 °C. The temperature increase will soften the PC and it will bend downwards. Thus, the contact area will increase into a circular shape and the meniscus will

flow completely over the flake/CNT. The glass slide/PC layer is retracted in a smooth motion and with it the flake/CNT as soon as the 124 ± 2 °C are reached. If desired, other flakes can be picked-up with this very stack by repeating the steps mentioned above. The drop-off is essentially a complete melting of the PC layer. First, flake(s)/CNT are aligned on the PC layer with the target substrate or graphite/bottom hBN bed. Second, the flake(s)/CNT are brought in contact with the substrate. Then the copper block is heated up to ~ 180 °C to melt the PC layer. When the PC layer is fully molten, one can retract the glass slide and the van der Waals stack remains under a circular PC coating. This PC coating can afterwards be dissolved in chloroform. Afterwards, an intermediate AFM imaging determines the exact position of transferred CNT. Since CNTs are invisible to the eye, it is absolutely necessary when transferring CNTs to mark down their exact position of the CNT relative to landmarks in the PC layer/PDMS lens and referencing the SEM pictures of the CNT. We achieve a consistent lateral precision of 5 μ m using our setup. This tolerance has to be kept in mind, when selecting suitable flakes.

EDGE CONTACT FABRICATION

For the fabrication of edge contacts on CNTs, NbSe₂ and graphite/graphene we use a common etching process based on reactive ion etching. This process is routinely used to make contacts on high-mobility hBN -encapsulated graphene devices. An application of this method to CNTs (where the edge

is limited to a handful of atoms) is reported by Huang *et al.* in Ref. [4]

The geometry of the electrodes is defined by EBL. To access the hBN-encapsulated target material we need to etch several nanometers (typically 10-20 nm) of hBN first. The etching process is realized in an Oxford Plasmlab 80 system. Before the actual etching process, we condition the chamber with an oxygen plasma (gas flow 100 sccm, chamber pressure 100 mTorr, power 200 W, process time 10 min). For the actual etching we use a mixture of CHF₃/O₂ gas with flow rates of 40 sccm/6 sccm (chamber pressure 35 mTorr, power 35 W, etching rate 0.45 nm/s). Immediately thereafter, the etched sample is mounted in a UHV chamber (pressure 1×10^{-6} mbar). A 1 nm high chromium layer is applied as an adhesion promoter, and 100 nm gold is applied as a contact metal. Before performing the lift-off, place the sample in acetone for one hour.

ATOMIC FORCE MICROSCOPY IMAGES

Figure S1 shows an atomic force microscopy (AFM) topography scan of a peripheral region of our device, where the CNT extrudes from the encapsulating van der Waals layers. In this area, the CNT lies directly on the SiO₂ substrate surface, where the CNT diameter can be more easily determined by AFM topography. From Fig. S1(a) we can determine the thickness of the lower hBN flake to ~ 35 nm (dark green dashed line), upper hBN flake to ~ 5 nm (light green dashed line) and the graphite flake ~ 40 nm (black dashed line). The CNT (white appearing line) is also visible. It is marked with white arrows. From the available AFM scans, we can estimate a CNT diameter of 1.2 ± 0.5 nm, see graph in Fig. S1(b). Despite the large error bar, we can conclude that (i) the diameter is well within the typical diameter distribution (1-2 nm) for our CVD growth process; (ii) though the presence of few bundled CNTs cannot be ruled out, the presence of many CNTs can definitely be excluded.

The current-independent value of the conductance between contacts 2-3 at low bias is roughly 120 e^2/h , which corresponds to a resistance of 215 Ω . Since at least 80 Ω are due to the cable resistance, we are left with more than 190 e^2/h ($\equiv 135 \Omega$) of sample conductance, which we attribute to the Au/NbSe₂ edge contact (typically of the order of hundreds of ohms for several micrometer-long edge contacts).

MEASUREMENT SETUP

Transport measurements were performed in a dilution refrigerator with a base temperature of 30 mK. We performed two-terminal measurements using a combined voltage excitation of a Stanford Lock-in amplifier SR830 (AC $20\mu V$, 27.77 Hz) mixed with a DC Signal from a Yokogawa 7651 DC voltage source (voltage division 1000/100). The current is converted back into a voltage signal and amplified by a Femto Ampere Current Amplifier DDPCA-300. AC component is detected with the same lock-in amplifier, whereas the DC part is measured with a Keysight 3458A Digital Multimeter. The graphite gate is biased with another Yokogawa 7651.

VOLTAGE VS CURRENT BIAS DEPENDENCE OF THE RESISTIVE PEAKS

Figure S2 shows that the differential resistance for contacts 1-2 (red), 1-3 (black) and 2-3 (green) as a function of (a) voltage bias and (b) current bias. Resistance peaks in the different traces are aligned on top of each other only when the resistance is plotted versus current and not voltage bias. This clearly indicates that the resistive features are triggered by current.

TEMPERATURE DEPENDENCE OF $G_{1-3}(I)$ AND $G_{2-3}(I)$

Figure S3 shows the temperature dependence of the differential conductance G(I) for the contact configurations 1-3 and 2-3, respectively. These measurements were taken in absence of magnetic field. Figure S3(a) shows that the minima in the differential conductance G are independent of the DC current up to at least 700 mK and disappear at latest 4.06 K. The temperature range between 800 mK and 4.2 K is difficult to access in a dilution refrigerator and therefore only these few measurement data are available for this measurement. Figure S3(b) shows that the high differential conductance of $\sim 120 \ e^2/h$ for small DC currents has a pronounced temperature dependence. The maximum conductance is maintained up to a temperature of 910 mK, even if the width of this maximum becomes smaller and at 560 mK two side maxima are visible. In contrast, the minima for $I_{DC} > 0.6 \ \mu A$ are independent for low temperatures up to more than 1 K. At 4.2 K these also disappear.

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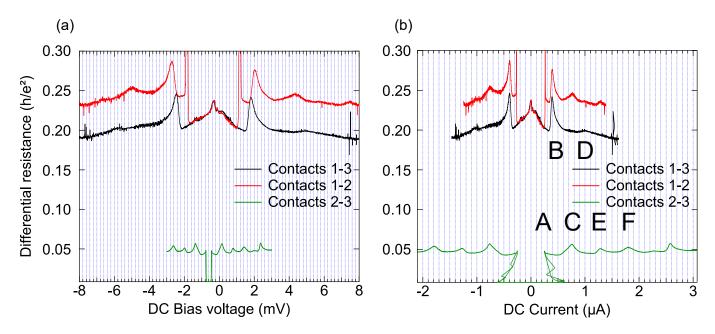


FIG. S2. Differential resistance as a function of (a) voltage bias and (b) current bias for contact configuration 1-3 (red), 1-2 (black) and 2-3 (green). Measurements were performed at 30 mK in absence of magnetic field. Various features of the different traces are aligned on top of each other only when plotted as a function of current bias.

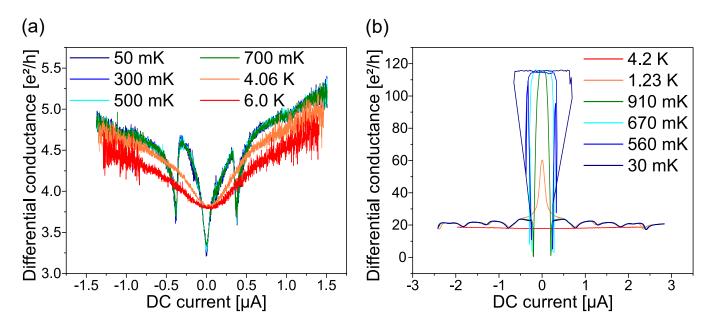


FIG. S3. Temperature dependence of the differential resistance for contacts (a) 1-3 and (b) 2-3, respectively. Measurements were taken in absence of magnetic field.